

What is claimed is:

1. A telecine display device comprising:

an input terminal to which is inputted a video signal having a telecine conversion applied from an original

5 picture image of a film source,

an A/D converter that converts the video signal into a digital video signal,

10 a telecine judgment circuit that judges whether the digital video signal outputted from the A/D converter is a telecine signal or not, and if it is the telecine signal, calculates a refresh rate of the original picture image of the film source,

15 a first synchronization processing circuit that outputs timing signals on the basis of the refresh rate of the video signal,

a second synchronization processing circuit that outputs timing signals on the basis of the refresh rate of the original picture image of the film source that the telecine judgment circuit calculated, and

20 a pull-down circuit that executes a 2-3 pull-down processing to the video signal in synchronization with the timing signals from the first synchronization processing circuit, stores the result in a first memory, and reads to output the video signal from the first memory in
25 synchronization with the timing signals from the second synchronization processing circuit.

2. A telecine display device as claimed in claim 1, wherein the video signal is outputted at the refresh rate of

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the original picture image of the film source.

3. A telecine display device as claimed in claim 1,
wherein the video signal is outputted at an integer-fold
refresh rate to the refresh rate of the original picture
5 image of the film source.

4. A telecine display device as claimed in claim 1,
further comprising a sub-field development circuit that
develops the video signal outputted from the pull-down
circuit into video signals by sub-fields each for gradation
10 display, and outputs to display obtained signals to a plasma
display module.

5. A telecine display device as claimed in claim 4,
wherein the sub-field development circuit has the refresh
rate thereof set to an integer-fold to the refresh rate of
15 the original picture image of the film source, and thereby
displays integer times the signal of the same frame with the
video signal outputted from the pull-down circuit, on the
plasma display module.

6. A telecine display device as claimed in claim 1,
20 wherein the pull-down circuit executes the 2-3 pull-down
processing and a progressive conversion processing to the
video signal from the telecine judgment circuit, and stores
obtained signals in the first memory.

7. A telecine display method comprising the steps
25 of:

applying a pull-down processing to a video signal
having a telecine conversion applied from an original
picture image of a film source by a first timing signal

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based on a refresh rate of the video signal, and

outputting to a display module the video signal
having the pull-down processing applied by a second timing
signal based on the refresh rate of the original picture
5 image of the film source.

8. A telecine display method comprising the steps
of:

calculating a first timing signal based on a refresh
rate of a video signal having a telecine conversion applied
10 from an original picture image of a film source, and a
second timing signal based on the refresh rate of the
original picture image of the film source,

applying a 2-3 pull-down processing to the video
signal in synchronization with the first timing signal, and
15 storing the video signal in a first memory, reading
out the video signal from the first memory on the basis of
the second timing signal, and outputting it to a display
module.

9. A telecine display method as claimed in claim 8,
20 wherein the video signal read from the first memory on the
basis of the second timing signal is developed into sub-
fields for gradation display in the sub-field development
circuit, and thereafter is outputted to a plasma display
module as a display module.